



**ANIL NEERUKONDA INSTITUTE OF TECHNOLOGY & SCIENCES**  
**"UGC AUTONOMOUS"**  
(Affiliated to Andhra University, Approved by AICTE & Accredited by NBA)  
Sangivalasa-531 162, Bheemunipatnam Mandal, Visakhapatnam Dt.  
**DEPARTMENT OF INFORMATION TECHNOLOGY**

Date: 18-08-2022

**CIRCULAR**

All the students of III B.Tech are requested to attend the In-house "GATE CLASSES" scheduled from 25<sup>th</sup> to 29<sup>th</sup> August 2022 respectively without fail.

Venue: Class Rooms

Subject	Faculty
Data Structures	A.Surekha
Operating Systems	D.Chandrika
Database Management System	V.Annapoorna
Computer Organization	K.Swathi

**HoD,IT**

Prof.P.Padmaja

**Prof. P.PADMAJA**  
head of the Department  
Department of IT  
ANITS Visakhapatnam



# ANILNEERUKONDA INSTITUTE OF TECHNOLOGY & SCIENCES 'UG AUTONOMOUS'

(Affiliated to AU, Approved by AICTE & Accredited by NBA & NAAC with 'A' Grade)

Sangivalasa-531162, Bheemunipatnam Mandal, Visakhapatnam Dt.

## Gate Classes Summary Report

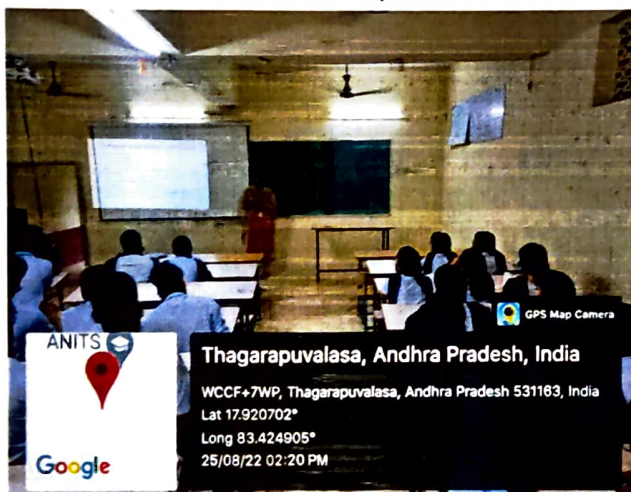
Day 1

Date: 25-08-2022	Number of Students: 44
Subject: Computer Organization	Topic: Instruction CPI and Memory Mapped IO

The Gate Classes are conducted as part of Gate Guidance to the students. In this session Instruction CPI and Memory Mapped IO concepts are given as a brief and made the students understand the concepts and made them clear how the concepts can be analyzed and answered if it is given in Gate.

The following concepts covered as part of Instruction CPI:

- What is Instruction CPI?
- What is CPI and MIPS?
- How do you calculate CPI in pipeline?
- How to calculate average CPI
- How does a memory map work?
- Tag directory size in mapping techniques
- Previous Gate Questions on Instruction CPI.



COMPUTER ORGANIZATION GATE CLASSES

DATE: 25/8/2021

TIME: 2:30-10:30

S.No	Roll No	Name of the Student	Signature
1	321126511L03	M. Sai Nithin	M. Sai Nithin
2	321126511L13	J. mouli	J. mouli
3	320126511155	R. Niharika	R. Niharika
4	320126511121	B. DIVYA	DIVYA
5	320126511118	B. poofa	B. poofa
6	320126511151	P. hema hamsika	P. hema hamsika
7	320126511142	M. Nanditha	M. Nanditha
8	320126511137	M. Chandini	M. Chandini
9	320126511163	V. Rikshitha	V. Rikshitha
10	320126511138	M. lakshmi prasanna	M. lakshmi prasanna
11	320126511159	S. Divyeeeda	S. Divyeeeda
12	320126511114	B. Tanusha	B. Tanusha
13	321126511L01	G. Koushik	G. Koushik
14	321126511L25	K. Suryn	K. Suryn
15	320126511128	G. Saurav Srinivas	G. Saurav Srinivas
16	320126511127	G. Eswar	G. Eswar
17	321126511L12	K. Likhith	K. Likhith
18	321126511L41	D. Mahesh Reddy	D. Mahesh Reddy
19	321126511L30	G. Sarinazi	G. Sarinazi
20	321126511L31	Harsha	Harsha
21	321126511L29	Durga Prasad	Durga Prasad
22	321126511L05	V. Rupesh Naga Sai Sai	V. Rupesh Naga Sai Sai
23	320126511036	M. V. Sri. Prudhvi	M. V. Sri. Prudhvi
24	320126511020	G. Abhish	G. Abhish
25	321126511L42	K. venu	K. venu
26	320126511062	B. Akash	B. Akash
27	321126511L32	B. Bhargav Sai	B. Bhargav Sai
28	321126511L40	K. Hazi	K. Hazi
29	320126511066	B. Poorna Srinidhi	B. Poorna Srinidhi
30	320126511031	K. Raghu Ram	K. Raghu Ram
31	321126511L04	K. Raghunatha Reddy	K. Raghunatha Reddy
32	320126511043	P. Sai Charan Vamsi	P. Sai Charan Vamsi
33	320126511041	P. Vipin	P. Vipin
34	320126511042	P. Kabeshwar	P. Kabeshwar
35	320126511008	B. Poorna Sai	B. Poorna Sai
36	320126511004	A. Raghav Ram	A. Raghav Ram
37	320126511011	Ch. Dhyanesh	Ch. Dhyanesh
38	320126511022	J. Prudhwanth	J. Prudhwanth
39	320126511019	D. Vivek Vardhan	D. Vivek Vardhan
40	320126511021	G. Ajay Kumar	G. Ajay Kumar
41	320126511022	G. Sai Ram	G. Sai Ram
42	320126511013	Ch. Chinmay	Ch. Chinmay
43	320126511035	M. Chandu	M. Chandu
44	320126511057	V. Rohit	V. Rohit

*[Handwritten signature]*

*[Handwritten signature]*  
 P. PADMAJA  
 Head of the Department  
 Department of IT  
 ANTC, Visakhapatnam

Gate Classes Summary Report

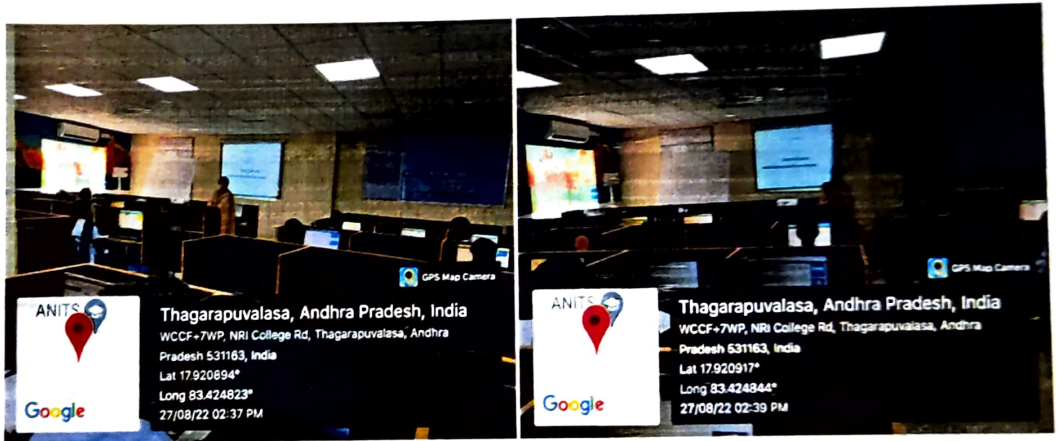
Day 2

Date: 26-08-2022	Number of Students: 44
Subject: Computer Organization	Topic: Cache Design and DMA

The Gate Classes are conducted as part of Gate Guidance to the students. In this session DMA concepts are given as a brief and made the students understand the concepts and analyzed the students the concepts importance and gave them important formulas in solving the Cache Design and DMA related problems.

The following concepts covered as part of CPU and I/O scheduling Algorithms:

- What is Cache Mapping
- Process of Cache Mapping
- Techniques of Cache Mapping
- DMA Transfer
- DMA Controller
- Previous Gate Questions on Cache mapping and DMA




**HEAD OF THE DEPARTMENT**  
Department of Information Technology  
Anil Neerukonda Institute of Technology  
& Sciences (Autonomous)  
Sangivalasa, Visakhapatnam-531162



Faculty  
K. Swathi Lakshmi Durga  
Assistant Professor  
Department of IT


**COMPUTER ORGANIZATION GATE CLASSES**

DATE: 26/8/2022

TIME: 1:40 to 2:30

S.No	Roll No	Name of the Student	Signature
1.	320126511056	Diana Janadala	Diana Janadala
	320126511064	B. Saketh	B. Saketh
	320126511072	E. Geethanand Dager	E. Geethanand Dager
	320126511073	G. Dhieraj Reddy	G. Dhieraj Reddy
	320126511147	N. Pravalika	N. Pravalika
	320126511150	Sharmila	Sharmila
	320126511051	Sauda. Thotam	Sauda. Thotam
	320126511046	R. Renu Cree	R. Renu Cree
	320126511131	Manaswini	Manaswini
	320126511116	Jayasree	Jayasree
	320126511066	B. padma sandeep	B. padma sandeep
	320126511132	K. Kanti sree	K. Kanti sree
	320126511062	B. Akash	B. Akash
	320126511117	B. Manasa Sri	B. Manasa Sri
	320126511123	D. Manjula	D. Manjula
	320126511183	K. Likhitha	K. Likhitha
	320126511029	K. Likhitha	K. Likhitha
	320126511038	M. Kavya Praneeetha	M. K. Praneeetha
	320126511096	P. Abhinav	P. Abhinav
	320126511135	Vashe	Vashe
	321126511130	G. Sai Hasi	G. Sai Hasi
	320126511048	S. Aparna	S. Aparna
	320126511075	J. Sandeep	J. Sandeep
	320126511143	N.V.S.C. Priya	Priya
	320126511145	R. Nanditha	R. Nanditha
	320126511049	S. Archana	S. Archana
	320126511105	S. Nihar	S. Nihar
	321126511129	Durga Prasad	Durga Prasad
	320126511032	M. Pavani	M. Pavani
	320126511077	K. Madhuri	K. Madhuri
	321126511136	P. Kavshik	P. Kavshik
	321126511137	G. Surya	G. Surya
	320126511006	B. H. Kalyani	B. H. Kalyani
	320126511073	K. Akash	K. Akash
	321126511131	P. Harsha	P. Harsha
	321126511145	P. Vamsi	P. Vamsi
	320126511059	V. Kavya	V. Kavya
	321126511134	G. Venkatesh	G. Venkatesh
	321126511146	B. Sujinvar	B. Sujinvar
	320126511026	J. Dikshita	J. Dikshita
	320126511093	P. Saikamal	P. Saikamal
	321126511139	T. Nimita Anwar Far	T. Nimita Anwar Far
	321126511133	S. Bhavani	S. Bhavani
	321126511106	G. Viraj	G. Viraj

*SA*

  
**PROF. P. PADMAJA**  
 head of the Department  
 Department of IT  
 www.vit.ac.in

Gate Classes Summary Report

Day 3

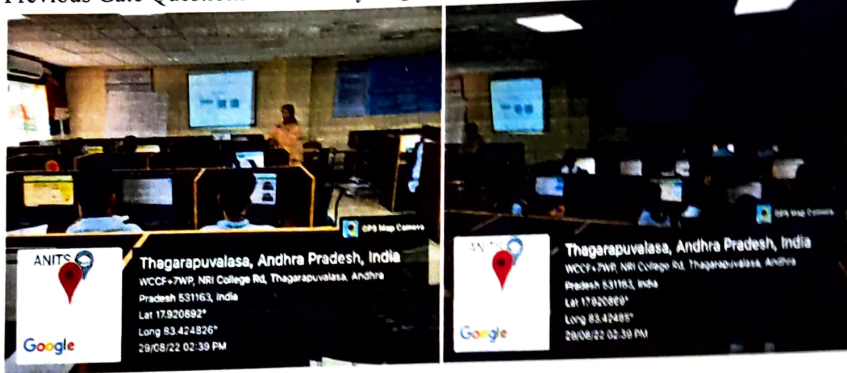
Date: 26-08-2022  
Subject: Computer Organization

Number of Students: 44  
Topic: Pipelining Instruction and Memory Organization

The Gate Classes are conducted as part of Gate Guidance to the students. In this session and Memory Organization concepts are given as a brief and made the students understand the concepts and made them clear how the concepts can be analyzed and answered if it is given in Gate.

The following concepts covered as part of Memory Organization and Pipelining:

- RISC & CISC
- Instruction Pipelining
- Pipeline & Vector Processing
- Memory Organization
- Previous Gate Questions on Memory Organization and Pipelining.



  
Faculty

Mrs. K. Swathi Lakshmi Durga  
Assistant Professor  
Department of IT

  
**HEAD OF THE DEPARTMENT**  
Department of Information Technology  
Anil Neerukonda Institute of Technology  
& Sciences (Autonomous)  
Sangivalasa, Visakhapatnam-531162

COMPUTER ORGANIZATION GATE CLASSES

DATE: 29/08/2022

TIME: 1:40 to 2:30

S.No	Roll No	Name of the Student	Signature
1	320126511058	V. Harshitha	V. Harshitha
2	320126511038	M. Kavya Praneetha	M. Kavya Praneetha
3	320126511076	K. V. C. Chandarathi	K. V. C. Chandarathi
4	320126511093	P. Saikamal	P. Saikamal
5	320126511028	K. Bhargavi	K. Bhargavi
	321126511127	P. Durga Prasad	P. Durga Prasad
	321126511126	M. Rahul	M. Rahul
	321265111123	D. Manjari	D. Manjari
	320126511029	K. Kikitha	K. Kikitha
	320126511134	K. Praneetha	K. Praneetha
	320126511115	B. Venkatesh	B. Venkatesh
	320126511120	B. Akash	B. Akash
	320126511137	Chandini	Chandini
	320126511056	Diana Javadala	Diana Javadala
	320126511078	K. Akash	K. Akash
	320126511116	Jayashree	Jayashree
	320126511146	M. Ashok	M. Ashok
	320126511148	Samath	Samath
	320126511051	Sauda Khatoon	Sauda Khatoon
	320126511046	R. Renu Sree	R. Renu Sree
	320126511162	Manikanta	Manikanta
	320126511153	P. Jaya Surya	P. Jaya Surya
	320126511049	S. Archana	S. Archana
	320126511064	B. Saketh	B. Saketh
	320126511102	P. Vivek	P. Vivek
	320126511156	Harsha	Harsha
	320126511048	S. Aparna	S. Aparna
	320126511140	A. Akhil	A. Akhil
	320126511032	M. Pavani	M. Pavani
	320126511087	Ch. Rishi	Ch. Rishi
	320126511149	N. Pradeep	N. Pradeep
	320126511006	B. H. Kalyani	B. H. Kalyani
	320126511131	G. Mandarini	G. Mandarini
	320126511156	R. Deva Babu	R. Deva Babu
	320126511112	Shyam Kumar	Shyam Kumar
	320126511059	V. Kavya	V. Kavya
	320126511089	M. Vinson	M. Vinson
	320126511164	Vinson	Vinson
	320126511111	A.P.V. S. Preetham	A.P.V. S. Preetham
	320126511026	J. Dikshita	J. Dikshita
	320126511075	J. Soudip	J. Soudip
	320126511096	P. Abhinav	P. Abhinav
	320126511105	S. Nihar	S. Nihar
	321126511106	Pranav	Pranav

*[Handwritten signature]*

*[Handwritten signature]*  
 Prof. P. PADMAJA  
 Head of the Department  
 Department of IT  
 West Hyderabad